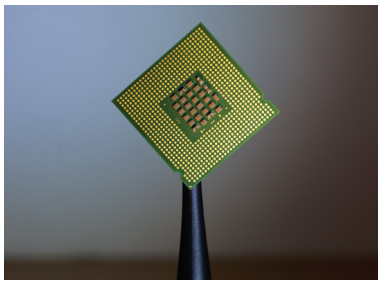


safest.taltech.ee

The SAFEST project has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 952252

COORDINATOR

TAL TECH

taltech.ee

PARTNERS

cnrs

cnrs.fr

UNIVERSITÉ MONTPELLIER

umontpellier.fr

TECHNISCHE UNIVERSITÄT MÜNCHEN

tum.de

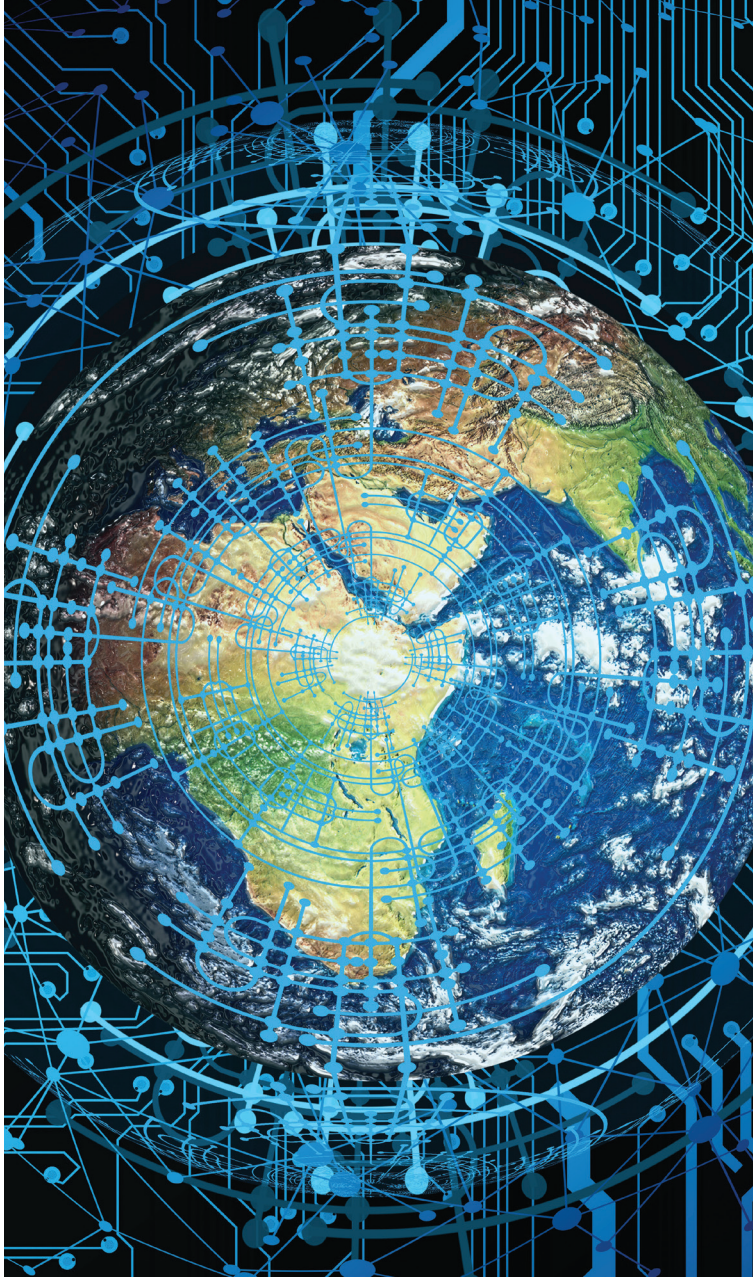
KU LEUVEN

kuleuven.be

TU Graz

Graz University of Technology

tugraz.at



SAFEST

THE OVERALL AIM

... of SAFEST is to enhance the scientific and technological capacity of Tallinn University of Technology (TalTech) in the field of Hardware Security, to be achieved through networking activities with its internationally leading Twinning partners. To achieve its overall aim, the 3 year project from 2021 to 2023 will build upon the existing strong competences of TalTech in closely related fields, to be complemented by the specific know-how of the Twinning partners.

RESEARCH ACTIVITIES

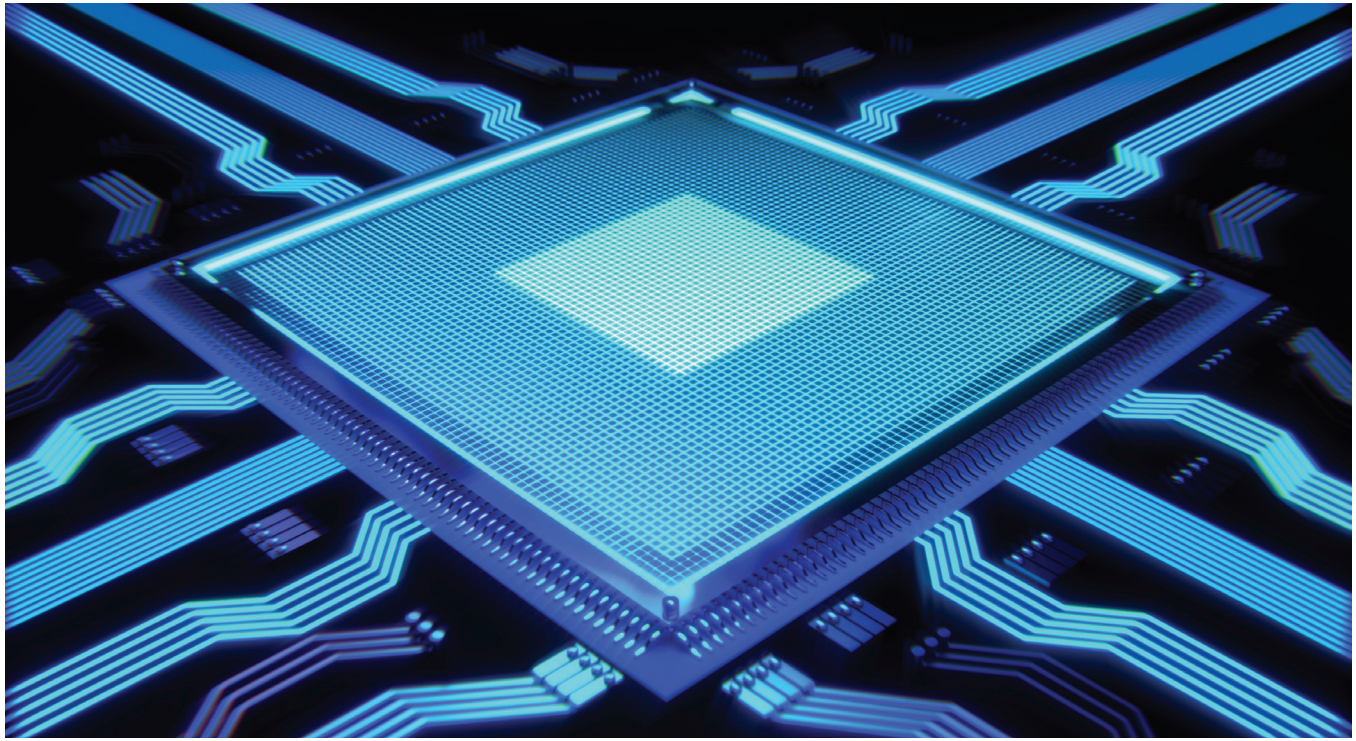
To boost their scientific excellence and innovation capacity, the partners will implement a networking strategy focused on four complementary subtopics:

1. **Test for security (CNRS/UM)**
2. **Reverse engineering and defences (TUM)**
3. **Side channel attacks (KU Leuven)**
4. **Hardware-software architectural vulnerabilities (TU Graz)**

PROJECT ACTIVITIES

In order to realize these objectives, the partners will implement a comprehensive set of measures via the following activities:

- Short term staff exchanges are focused on providing education and knowledge exchange concerning the identified research subtopics in Hardware Security.
- Short term exchanges of early stage researchers (ESRs) will allow TalTech to host bright ESRs from the internationally leading partners and vice-versa. All exchanges are within the scope of the Hardware Security research subtopics collectively identified by the partners
- Organization of workshops. The training workshops will deal with cross training on the four research subtopics, plus more general innovation skills. One workshop a year will be conducted to support the researchers of all partners.
- Three summer schools will be organised over the course of the project. The course materials will cover the very latest findings on Hardware Security including challenges in post-quantum cryptography, detection of hardware trojan horses by unconventional means, FPGAs as hybrid obfuscation solutions against IP piracy, and logic locking and prevalent variants.



FIND OUT MORE AT:

SAFEST.TALTECH.EE

OR CONTACT US:

SAMUEL PAGLIARINI
Project Coordinator
samuel.pagliarini@taltech.ee

TALLINN UNIVERSITY OF TECHNOLOGY
Ehitajate tee 5, 19086 Tallinn
Estonia